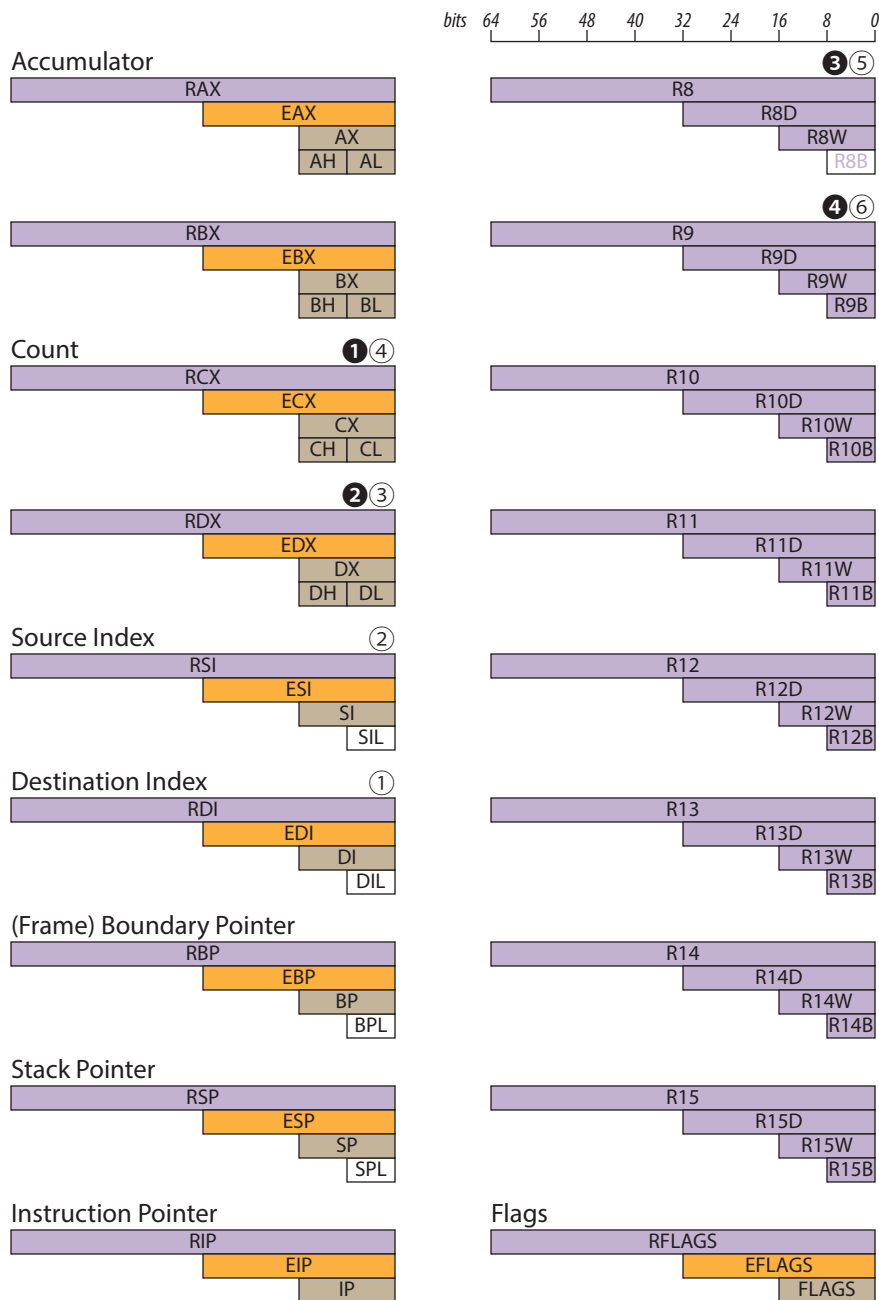
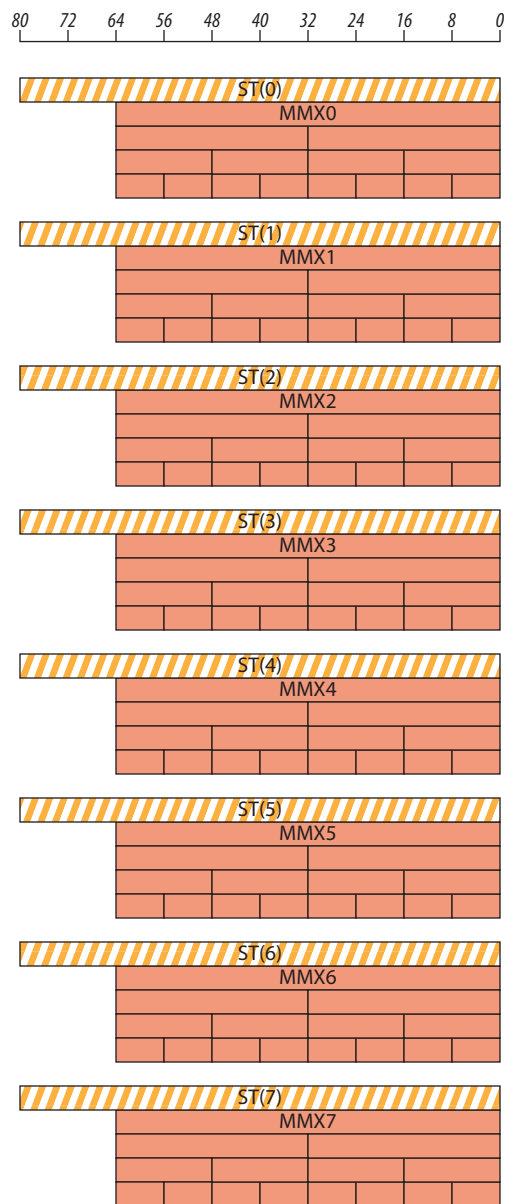


# x86 Register Model

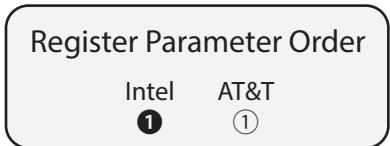
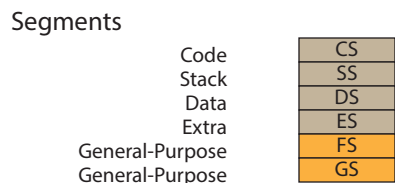
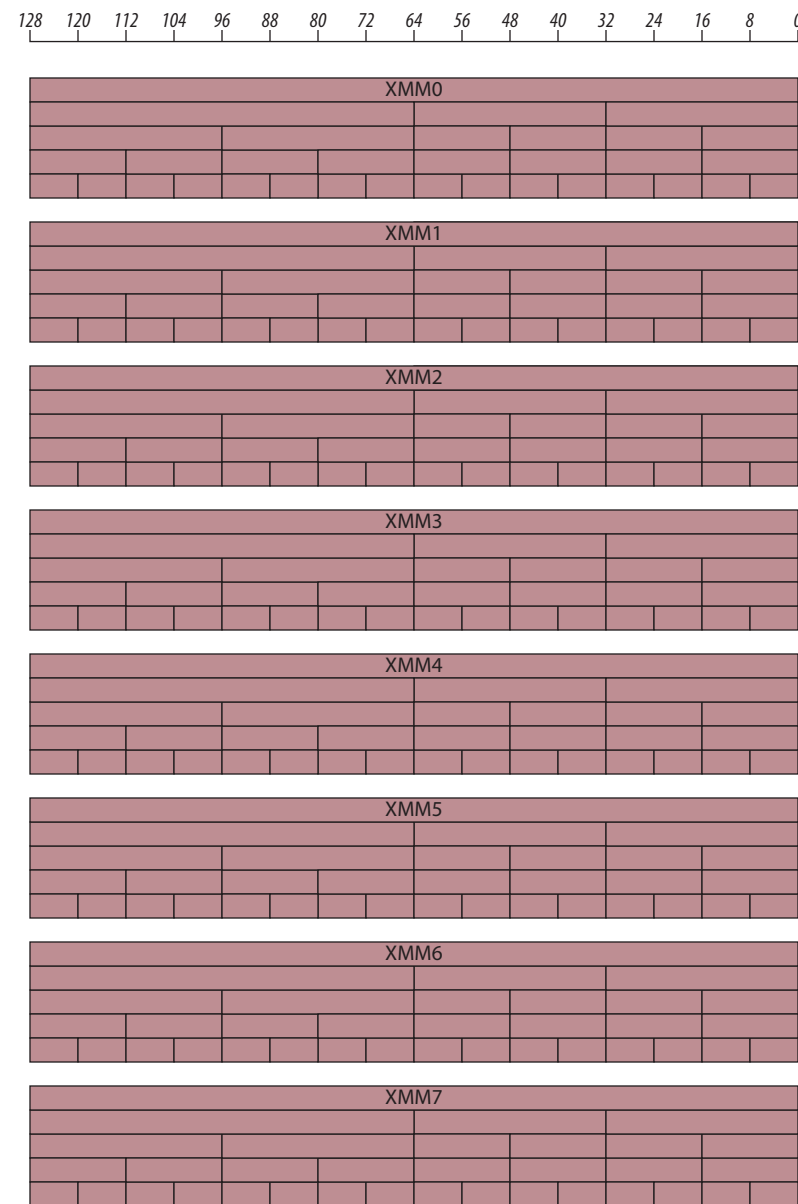
## General-Purpose Registers



## FP Stack/MMX Registers



## Streaming SIMD Registers



Syntax	Intel	AT&T	Intel	AT&T
<i>immediate</i>	42	\$42	8-bit	byte ptr b
<i>absolute register</i>	12345678	*12345678	16-bit	word ptr w
<i>register</i>	eax	%eax	32-bit	dword ptr l
<i>order</i>	dest,src	src,dest	64-bit	qword ptr q
<i>example</i>	mov eax,4	movl \$4,%eax		

